

**FEE TRANSMITTAL
FOR FY 2001**

APR 30 2001

Patent fees are subject to annual revision.

Complete if Known

Application Number	08/520,079
Filing Date	August 28, 1995
First Named Inventor	Shunpei YAMAZAKI et al.
Examiner Name	J. Jackson, Jr.
Group Art Unit	2815
Attorney Docket No.	740756-001400

TOTAL AMOUNT OF PAYMENT (\$ 290.00)

METHOD OF PAYMENT

- 1.
- ☐
- The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit
Account
Number

19-2380

Deposit
Account
Name

Nixon Peabody LLP

- ☒ Charge Any Additional Fee Required
Under 37 CFR 1.16 and 1.17
- ☐ Applicant claims small entity status.
See 37 CFR 1.27

- 2.
- ☒
- Payment Enclosed:

- ☒ Check ☐ Credit Card ☐ Money
Order ☐ Other

FEE CALCULATION**1. BASIC FILING FEE**

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
101	710	201	355	Utility filing fee	
106	320	206	160	Design filing fee	
107	490	207	245	Plant filing fee	
108	710	208	355	Reissue filing fee	
114	150	214	75	Provisional filing fee	

SUBTOTAL (1) (\$ 0)

2. EXTRA CLAIM FEES

Total Claims	Extra Claims	Fee from below	Fee Paid
72 ** =	0 X	0 =	0
Independent Claims 10 ** =	0 X	0 =	0
Multiple Dependent	0 =	0	0

Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description
103	18	203	9	Claims in excess of 20
102	80	202	40	Independent claims in excess of 3
104	270	204	135	Multiple dependent claim, if not paid
109	80	209	40	** Reissue independent claims over original patent
110	18	210	9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$ 0)

**or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)**3. ADDITIONAL FEES**

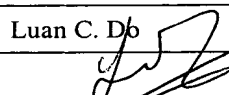
Large Entity Fee Code	Fee (\$)	Small Entity Fee Code	Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English transaction	
147	2,520	147	2,520	For filing a request for <i>ex parte</i> reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	110.00
116	390	216	195	Extension for reply within second month	
117	890	217	445	Extension for reply within third month	
118	1,390	218	695	Extension for reply within fourth month	
128	1,890	228	945	Extension for reply within fifth month	
119	310	219	155	Notice of Appeal	
120	310	220	155	Filing a brief in support of an appeal	
121	270	221	135	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,240	241	620	Petition to revive - unintentional	
142	1,240	242	620	Utility issue fee (or reissue)	
143	440	243	220	Design issue fee	
144	600	244	300	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Petitions related to provisional applications	
126	180	126	180	Submission of Information Disclosure Stmt	\$180.00
581	40	581	40	Recording each patent assignment per property (times number of properties)	
146	710	246	355	Filing a submission after final rejection (37 CFR § 1.129(a))	
149	710	249	355	For each additional invention to be examined (37 CFR § 1.29(b))	
179	710	249	355	Request for Continued Examination (RCE)	
169	900	169	900	Request for expedited examination of a design application	

Other fee (specify) _____

* Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$ 290.00)

SUBMITTED BY**Complete (if applicable)**

Name (Print/Type)	Luan C. Do	Registration No. (Attorney/Agent)	38,434	Telephone	703-790-9110
Signature				Date	April 26, 2001

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40/Thusa
5-8-01

**BOX AFTER FINAL
PATENT
740756-001400**

UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Shunpei YAMAZAKI et al.

Application No.: 08/520,079

Filed: August 28, 1995

For: SEMICONDUCTOR CIRCUIT
FOR ELECTRO-OPTICAL DEVICE)
AND METHOD OF
MANUFACTURING THE SAME)

) Art Unit: 2815

) Examiner: J. Jackson, Jr.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 4/26/01
Shunpei Yamazaki

AMENDMENT

Commissioner of Patents
Washington, D.C. 20231

APR 26 2001
RECEIVED
COMM. FOR PAT. & TRADEMARKS
D.C. 20231

Dear Sir:

In response to the Examiner's Final Office Action mailed December 26, 2000, the period for responding having been extended one (1) month, please consider the following amendments and remarks in connection with the above-identified application.

IN THE CLAIMS:

Please amend claims 73, 80, 87, 93, 99, 105, 111, 117, 123 and 129 as follows:

73. (Thrice Amended) A thin film transistor comprising:

a crystalline semiconductor island over a substrate having an insulating surface;

source and drain regions in said semiconductor island;

a channel forming region between said source and drain regions;

a gate insulating film adjacent to at least said channel forming region;

a gate electrode adjacent to said channel forming region having said gate insulating film

therebetween,